

INTERNATIONAL STANDARD

REDLINE VERSION

Ferrite cores - Standard inductance factor for gapped cores and its tolerance

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INTERNATIONAL ELECTROTECHNICAL COMMISSION

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FOREWORD

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This redline version of the official IEC Standard allows the user to identify the changes made to the previous edition IEC 62358:2012. A vertical bar appears in the margin wherever a change has been made. Additions are in green text, deletions are in strikethrough red text.

IEC 62358 has been prepared by IEC technical committee 51: Magnetic components, ferrite and magnetic powder materials. It is an International Standard.

This third edition cancels and replaces the second edition published in 2012. This edition constitutes a technical revision.

This edition includes the following significant technical changes with respect to the previous edition:

- a) addition of A_L value (inductance factor) and its tolerance for PM-cores;
- b) addition of A_L value (inductance factor) and its tolerance for EC-cores;
- c) addition of A_L value (inductance factor) and its tolerance for EFD-cores.

The text of this International Standard is based on the following documents:

Draft	Report on voting
51/1583/FDIS	51/1595/RVD

Full information on the voting for its approval can be found in the report on voting indicated in the above table.

The language used for the development of this International Standard is English.

This document was drafted in accordance with ISO/IEC Directives, Part 2, and developed in accordance with ISO/IEC Directives, Part 1 and ISO/IEC Directives, IEC Supplement, available at www.iec.ch/members_experts/refdocs. The main document types developed by IEC are described in greater detail at www.iec.ch/publications.

The committee has decided that the contents of this document will remain unchanged until the stability date indicated on the IEC website under webstore.iec.ch in the data related to the specific document. At this date, the document will be

- reconfirmed,
- withdrawn, or
- revised.

INTRODUCTION

~~The A_L value (inductance factor) and its tolerance have been specified by the users before. When manufacturers wish to have an inventory for short delivery, they have to hold the products before gapping since there is no standard for the A_L value. Because of electronic commerce and the increased demand for rapid delivery of products, it will be more convenient for customers and suppliers to refer to established A_L values and tolerances. This standard has been developed to meet this demand.~~

~~As a result of the implementation of this standard, it will be easier for core suppliers and users to develop electronic components using gapped soft ferrite cores. Conventional businesses will benefit, as will new companies working in new fields such as e-commerce.~~

~~It is recommended that users specify A_L values by selecting them from this standard. The tolerances in this standard are recommended, but for historical reasons a manufacturer's specification might differ for some components. Users should confirm tolerances from the manufacturer's literature. Manufacturers are encouraged to use the A_L values in this standard when building stocks of gapped cores for short delivery. In cases where users or manufacturers specify a gap length with tolerances the A_L value will only be approximate and without tolerance. Such cases will be outside the scope of this standard.~~

The A_L value (inductance factor) and its tolerance is a critical design parameter for gapped ferrite core sets. For any core size, there is a wide range of possible gapped A_L values, and there is no technical reason why a user could not select any precise value within the possible range. But as a practical matter, there are advantages to the users that certain values within the range are identified to be standard and to the users in selecting those values where possible.

The potential advantages include faster delivery from supplier inventories of standard gapped parts, greater ease of obtaining small quantities for development sampling and low-volume projects, interchangeability among multiple applications, reduced risk of dead inventories, economies of scale from producing larger batches, and standardized tolerancing across multiple suppliers.

It is important that users specify A_L values by selecting them from this document when possible. The tolerances shown in this document are selected for suitable performance with best cost and full interchangeability. Suppliers may offer tighter tolerances for their standard parts while remaining in compliance with the standard. Users can require tighter tolerances for specific applications.

In cases where users or suppliers specify a physical gap length with tolerances, then the A_L value will only be approximate and without tolerance. It is generally true that better measurement precision and better consistency in application performance is achieved by specifying the A_L value and not the gap dimension. The exceptions are cases in which the gap is quite large, where users can find that their application coil gives a more consistent result if the gap dimension is controlled directly and the A_L value as measured in the supplier's standard coil is allowed to vary.

1 Scope

This document provides standard A_L values (inductance factors) and their tolerances of Pot, RM, ETD, E, EER, EP, PQ, PM, EC, EFD and low-profile gapped ferrite cores.

2 Normative references

The following documents are referred to in the text in such a way that some or all of their content constitutes requirements of this document. For dated references, only the edition cited applies. For undated references, the latest edition of the referenced document (including any amendments) applies.

~~IEC 61185, Ferrite cores (ETD-cores) intended for use in power supply applications — Dimensions~~

~~IEC 61596, Magnetic oxide EP-cores and associated parts for use in inductors and transformers — Dimensions~~

IEC 62044-2, *Cores made of soft magnetic materials - Measuring methods - Part 2: Magnetic properties at low excitation level*

~~IEC 62317-2, Ferrite cores — Dimensions — Part 2: Pot-cores for use in telecommunications, power supply, and filter applications~~

~~IEC 62317-4, Ferrite cores — Dimensions — Part 4: RM-cores and associated parts~~

~~IEC 62317-7, Ferrite cores — Dimensions — Part 7: EER-cores~~

~~IEC 62317-8, Ferrite cores — Dimensions — Part 8: E-cores~~

~~IEC 62317-9, Ferrite cores — Dimensions — Part 9: Planar cores~~

~~IEC 62317-13, Ferrite cores — Dimensions — Part 13: PQ-cores for use in power supply applications~~

ISO 497, *Guide to the choice of series of preferred numbers and of series containing more rounded values of preferred numbers*

Bibliography

IEC 63093-2, *Ferrite cores - Guidelines on dimensions and the limits of surface irregularities - Part 2: Pot-cores for use in telecommunications, power supply, and filter applications*

IEC 63093-4, *Ferrite cores - Guidelines on dimensions and the limits of surface irregularities - Part 4: RM-cores*

IEC 63093-5, *Ferrite cores - Guidelines on dimensions and the limits of surface irregularities - Part 5: EP-cores and associated parts for use in inductors and transformers*

IEC 63093-6, *Ferrite cores - Guidelines on dimensions and the limits of surface irregularities - Part 6: ETD-cores for use in power supplies*

IEC 63093-7, *Ferrite cores - Guidelines on dimensions and the limits of surface irregularities - Part 7: EER-cores*

IEC 63093-8, *Ferrite cores - Guidelines on dimensions and the limits of surface irregularities - Part 8: E-cores*

IEC 63093-9, *Ferrite cores - Guidelines on dimensions and the limits of surface irregularities - Part 9: Planar cores*

IEC 63093-10, *Ferrite cores - Guidelines on dimensions and the limits of surface irregularities - Part 10: PM-cores and associated parts*

IEC 63093-11, *Ferrite cores - Guidelines on dimensions and the limits of surface irregularities - Part 11: EC-cores for use in power supply applications*

IEC 63093-13, *Ferrite cores - Guidelines on dimensions and the limits of surface irregularities - Part 13: PQ-cores*

IEC 63093-14, *Ferrite cores - Guidelines on dimensions and the limits of surface irregularities - Part 14: EFD-cores*